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January 17, 2006

From: William A. Rudy

Matter No.: 400646

To:	Company:	Fax Number:	Phone Number:
Commissioner For Patents	U.S. Patent Office	(571) 273-8300	

Number of Pages Transmitted (including this cover sheet): 5

Message:

Applicant(s):	S. Brandon Keller	Confirmation No.	2411
Serial No.	10/647,597	Group Art Unit:	2825
Filed:	August 25, 2003	Examiner:	Magid Y. Dimyan
For:	System And Method For Determining Wire Capacitance For VLSI Circuit	Attorney Docket No.	100111221-1

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NO. 2523 P. 2/5

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

JAN 17 2006

PATENT APPLICATION

ATTORNEY DOCKET NO. 100111221-1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): S. Brandon Keller, et al.

Confirmation No.: 2411

Application No.: 10/647,597

Examiner: Magid Y. Dimyan

Filing Date: Aug. 25, 2003

Group Art Unit: 2825

Title: System And Method For Determining Wire Capacitance For VLSI Circuit

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- () under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- (X) under 37 CFR 1.97 (c) together with either a:
 - (X) Statement under 37 CFR 1.97(e), or
 - () a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- () under 37 CFR 1.97 (d) together with a:
 - () Statement under 37 CFR 1.97(e)(1) or (2), and
 - () a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account 08-2025 the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25.

(X) Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

() A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

- () I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450. Date of Deposit: _____

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Typed Name: Janet Ridpath

Signature: Janet Ridpath

Respectfully submitted,

S. Brandon Keller, et al.

By William A. Rudy

William A. Rudy

Attorney/Agent for Applicant(s)

Reg. No. 34,916

Date: Jan. 17, 2006

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HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

NO. 2523 P. 3/5
PATENT APPLICATION

ATTORNEY DOCKET NO. 100111221-1

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): S. Brandon Keller, et al.

Confirmation No.: 2411

Application No.: 10/647,597

Examiner: Magid Y. Dimyan

Filing Date: Aug. 25, 2003

Group Art Unit: 2825

Title: System And Method For Determining Wire Capacitance For VLSI Circuit

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S. Brandon Keller, et al.

By William A. Rudy

William A. Rudy

Attorney/Agent for Applicant(s)

Reg. No. 34,916

Date: Jan. 17, 2006

JAN 17 2006

PTO/SE/D8a (08-03)

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**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet	1
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1

of	1
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1

Complete if Known

Application Number

10/647,597

Filing Date

August 25, 2003

First Named Inventor

S. Brandon Keller

Art Unit

2025

Examiner Name

Magid Y. Dimyan

Attorney Docket Number

100111221-1

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional).² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 601.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Title: System And Method For Determining Wire Capacitance For VLSI Circuit

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Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

STATEMENT UNDER 37 CFR 1.97(e)

Sir:

The undersigned hereby certifies that either:

(X) Each item of information contained in the Information Disclosure Statement was cited for the first time in a communication from a foreign patent office in a counterpart foreign application, not more than three months prior to the filing of the statement, or

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-- to the knowledge of the undersigned, after making reasonable inquiry, was known to any individual designated in 37 CFR 1.56 (a) more than three months prior to the filing of the Information Disclosure Statement.

Respectfully submitted,

S. Brandon Keller, et al.

By


William A. Rudy

Attorney/Agent for Applicant(s)
Reg. No. 34,916

Date: Jan. 18, 2006

Telephone No.: (816) 460-5819